

DEVICE INFORMATION

Part Number	Top Mark	R _{ON} (Typ) V _{BAT} =3.6V	Threshold V _{TH_OFF}
GLF73510	UA	30 mΩ	3.05 V

FUNCTIONAL BLOCK DIAGRAM

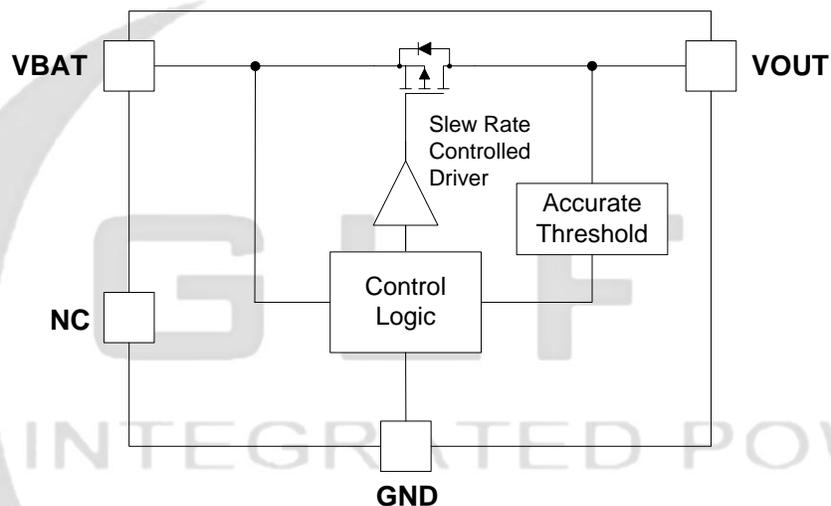
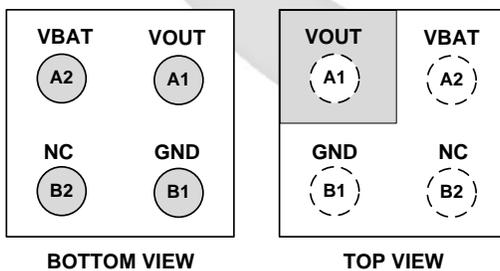


Figure 1. Functional Block Diagram

PIN CONFIGURATION

PIN DEFINITION



Pin #	Name	Description
A1	V _{OUT}	V _{OUT} pin is connected to the charger output and system load. If the switch is in the off state applying the appropriate voltage (V _{ON}) to V _{OUT} will turn the switch back on.
A2	V _{BAT}	V _{BAT} pin is connected to the battery pack's positive input to monitor the battery voltage. When the V _{BAT} voltage reaches the V _{TH_OFF} , the main switch is turned off immediately and latches to save the battery from discharging.
B1	GND	Ground
B2	NC	No Connection

Figure 2. 0.97 mm x 0.97 mm x 0.55 mm WLCSP

ABSOLUTE MAXIMUM RATINGS

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions; extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Min.	Max.	Unit
V _{BAT} , V _{OUT}	Each Pin Voltage to GND	-0.3	6	V
I _{OUT}	Maximum Continuous Switch Current		2	A
P _D	Power Dissipation at T _A = 25°C		1.2	W
T _{STG}	Storage Junction Temperature	-65	150	°C
T _A	Operating Temperature Range	-40	85	°C
θ _{JA}	Thermal Resistance, Junction to Ambient		85	°C/W
ESD	Electrostatic Discharge Capability	Human Body Model, JESD22-A114	6	kV
		Charged Device Model, JESD22-C101	2	

ELECTRICAL CHARACTERISTICS

Values are at T_A = 25 °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit	
V _{BAT} , V _{OUT}	Operating Voltage		1.1		5.5	V	
V _{TH_OFF}	OFF Threshold Voltage	V _{BAT} decreases until switch turns OFF	2.85	3.05	3.20	V	
		V _{BAT} decreases until switch turns OFF, T _a =55 °C ⁽¹⁾		2.9			
V _{ON}	ON Voltage applied to V _{OUT}	V _{OUT} increases until switch turns ON, V _{BAT} = 3.6 V ⁽²⁾		3.5		V	
		V _{OUT} increasing, V _{BAT} = 3.6 V, T _a =55 °C ⁽¹⁾		3.4			
I _q	Quiescent Current with Switch On	V _{BAT} = 3.6 V, I _{OUT} =0 mA, Switch = ON		0.51		uA	
		V _{BAT} = 4.2 V, I _{OUT} =0 mA, Switch = ON		0.73	1		
		V _{BAT} = 4.2 V, I _{OUT} =0 mA, Switch = ON, T _a =55 °C ⁽¹⁾		0.70			
		V _{BAT} = 4.2 V, I _{OUT} =0 mA, Switch = ON, T _a =85 °C ⁽¹⁾		0.69			
I _{SD}	Shutdown Current	V _{BAT} = 1.1 V, V _{OUT} = 0 V		2		nA	
		V _{BAT} = 2.5 V, V _{OUT} = 0 V		3			
		V _{BAT} = 3.3 V, V _{OUT} = 0 V		5			
		V _{BAT} = 3.6 V, V _{OUT} = 0 V		6			
		V _{BAT} = 4.2 V, V _{OUT} = 0 V		9	50		
		V _{BAT} = 4.2 V, V _{OUT} = 0 V, T _a =55 °C ⁽¹⁾		0.06		uA	
		V _{BAT} = 4.2 V, V _{OUT} = 0 V, T _a =85 °C ⁽¹⁾		0.55			
R _{ON}	On-Resistance	V _{BAT} =4.2 V, I _{OUT} = 500 mA	T _a =25 °C		28	32	mΩ
			T _a =55 °C ⁽¹⁾		30		
			T _a =85 °C ⁽¹⁾		33		
		V _{BAT} = 3.6 V, I _{OUT} = 500 mA	T _a =25 °C		30	34	
			T _a =55 °C ⁽¹⁾		32		
			T _a =85 °C ⁽¹⁾		35		
		V _{BAT} = 3.3 V, I _{OUT} = 500 mA	T _a =25 °C		31	35	
V _F	Forward Voltage of Diode ⁽¹⁾	I _F = 5 mA		0.4		V	
t _{OFF}	Turn-Off Time ⁽¹⁾	C _{OUT} =0.1 μF, R _{OUT} =150 Ω, V _{OUT} = V _{TH_OFF} to 0 V		36		us	

Notes: 1. By design; characterized, not production tested. 2. See Figure 10 for details.

TYPICAL PERFORMANCE CHARACTERISTICS

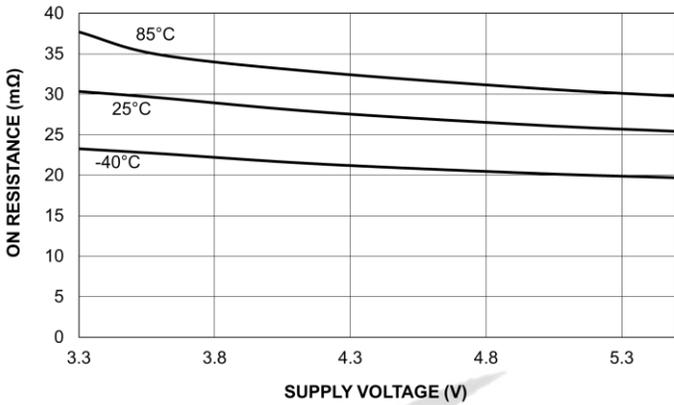


Figure 3. On-Resistance vs. Supply Voltage, V_{BAT}

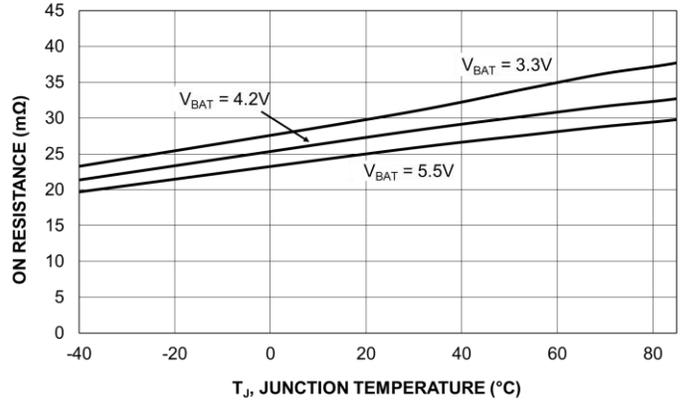


Figure 4. On-Resistance vs. Temperature

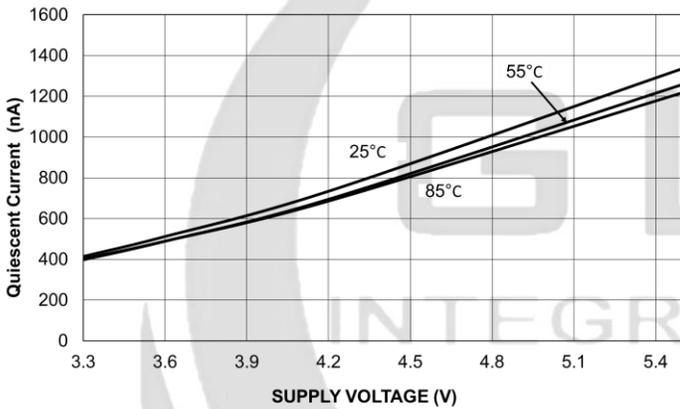


Figure 5. Quiescent Current vs. Supply Voltage, V_{BAT}

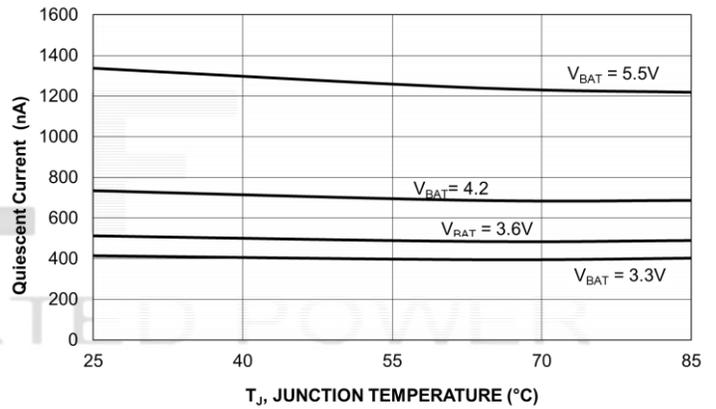


Figure 6. Quiescent Current vs. Temperature

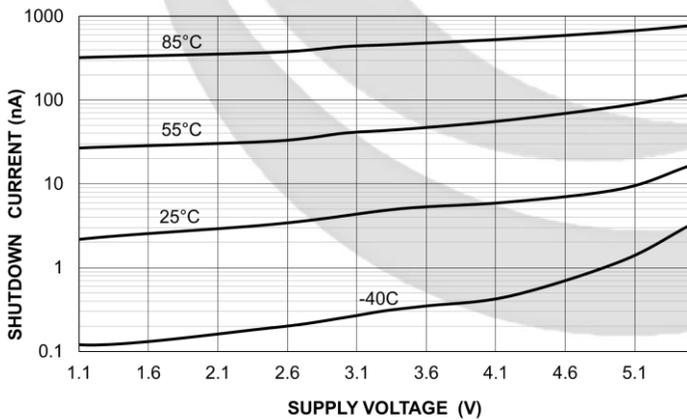


Figure 7. Shutdown Current vs. V_{BAT}

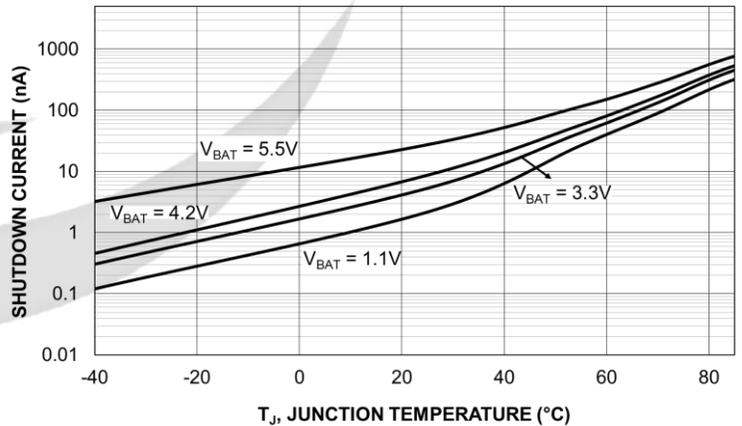


Figure 8. Shutdown Current vs. Temperature

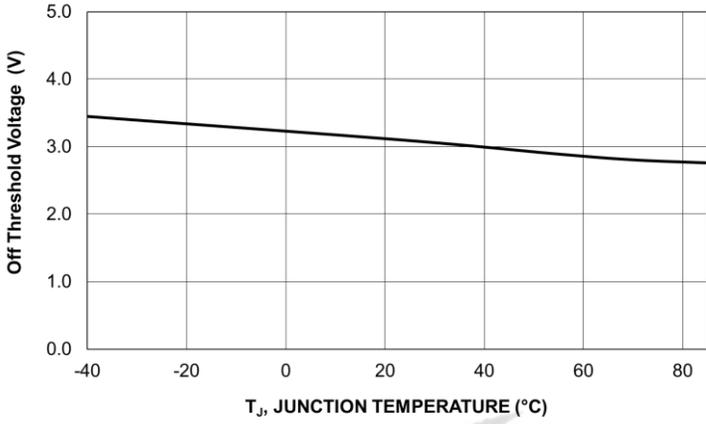


Figure 9. Off Threshold Voltage vs. Temperature

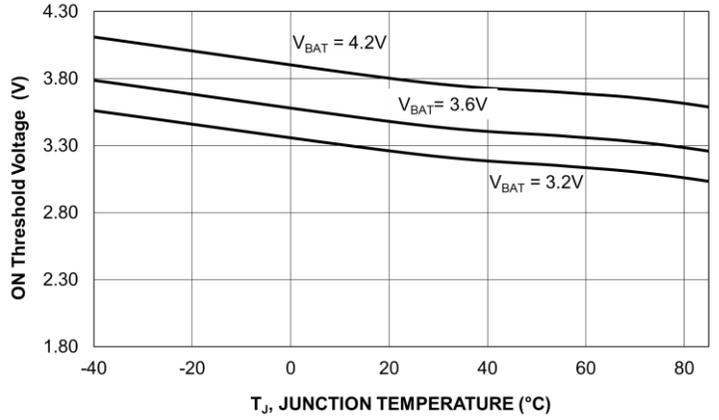


Figure 10. On Voltage vs. Temperature

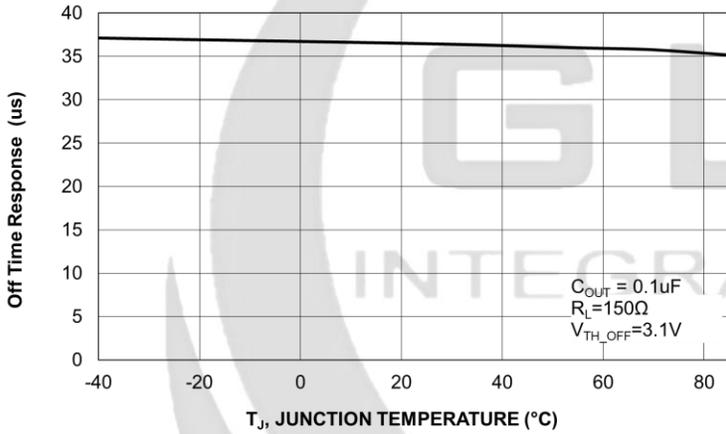


Figure 11. Off Time Response

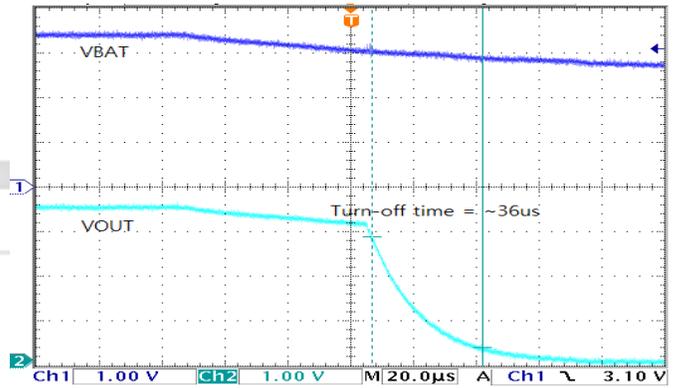


Figure 12. Turn Off Time, $C_{OUT}=0.1\mu F$, $R_L=150\Omega$

APPLICATION INFORMATION

The GLF73510 is an ultra-efficiency, 2 A rated, load switch with accurate ON/OFF threshold voltage and integrated slew rate control. The best in class efficiency makes it an ideal choice for use in wearables, IoT devices, and mobile electronics.

ON / OFF Threshold

When the voltage of a battery decreases to the off threshold voltage level, the GLF73510 is turned off, consuming an ultra-low leakage current to save the battery. The GLF73510 remains in the off state until a higher voltage is applied to the V_{OUT} pin. Note that the GLF73510 is enabled only by the V_{OUT} pin with a higher voltage than the on threshold voltage. With the higher V_{OUT} voltage from a charger applied, the GLF73510 is fully turned on and monitors the V_{BAT} voltage. When the V_{OUT} voltage of a charger is less than the on threshold voltage, a battery can be charged through the body diode of the main switch.

Output Capacitor

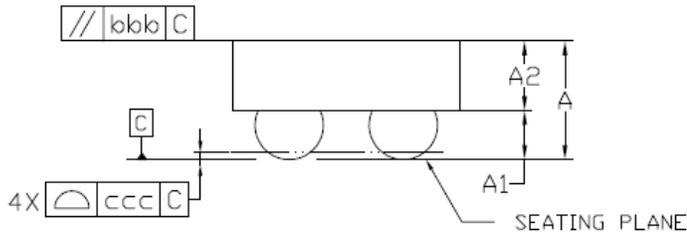
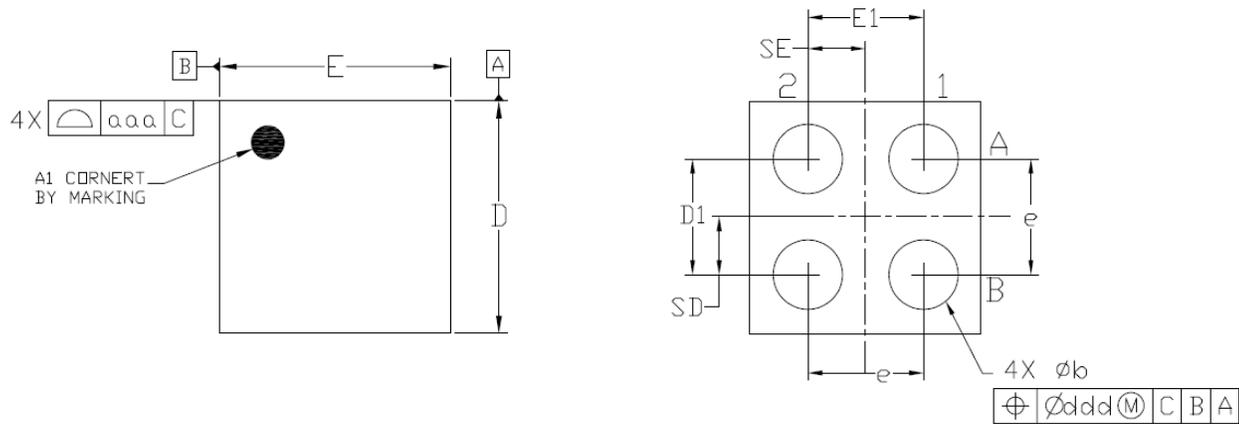
An output capacitor is not required for GLF73510 operation. However, a 0.1 μ F capacitor is recommended to be placed close to the V_{OUT} pin to mitigate an undershoot voltage or the transient voltage peak caused by a hot-plugging voltage source.

Board Layout

All traces should be as short as possible to minimize parasitic inductance effects. Wide traces for V_{BAT} , V_{OUT} , and GND will help reduce voltage drops, and parasitic effects during dynamic operation as well as improve the thermal performance at high load currents.

GLF
INTEGRATED POWER

PACKAGE OUTLINE



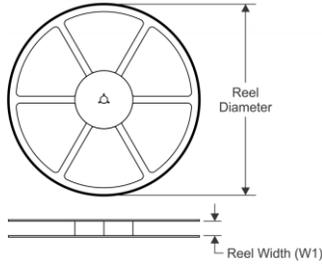
Dimensional Ref.			
REF.	Min.	Nom.	Max.
A	0.500	0.550	0.600
A1	0.225	0.250	0.275
A2	0.275	0.300	0.325
D	0.960	0.970	0.985
E	0.960	0.970	0.985
D1	0.450	0.500	0.550
E1	0.450	0.500	0.550
b	0.260	0.310	0.360
e	0.500 BSC		
SD	0.250 BSC		
SE	0.250 BSC		
Tol. of Form&Position			
aaa	0.10		
bbb	0.10		
ccc	0.05		
ddd	0.05		

Notes

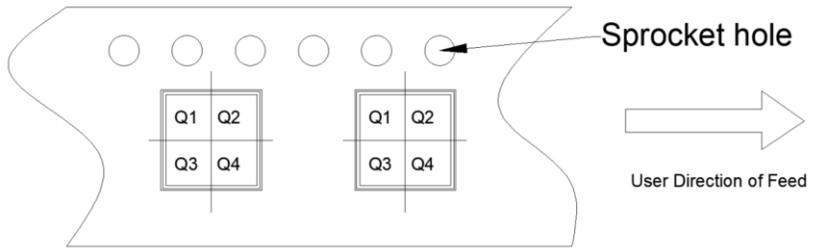
1. ALL DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
2. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M-1994.

TAPE AND REEL INFORMATION

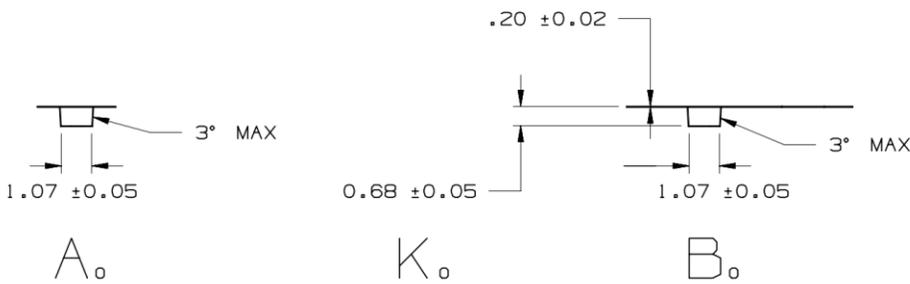
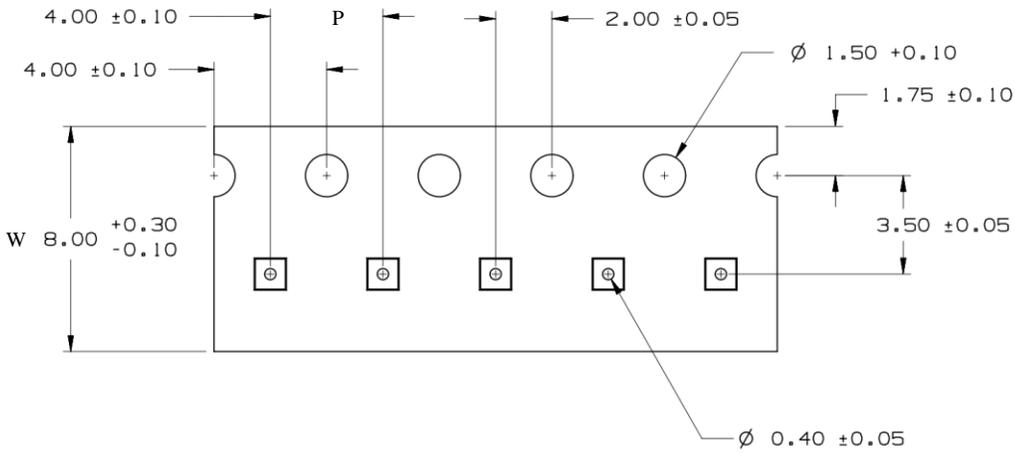
REEL DIMENSIONS



QUADRANT ASSIGNMENTS PIN 1 ORIENTATION TAPE



TAPE DIMENSIONS



Device	Package	Pins	SPQ	Reel Diameter(mm)	Reel Width W1	A0	B0	K0	P	W	Pin1
GLF73510	WLCSP	4	3000	180	9	1.07	1.07	0.68	4	8	Q1

Remark:

- A0: Dimension designed to accommodate the component width
- B0: Dimension designed to accommodate the component length
- C0: Dimension designed to accommodate the component thickness
- W: Overall width of the carrier tape
- P: Pitch between successive cavity centers

SPECIFICATION DEFINITIONS

Document Type	Meaning	Product Status
Target Specification	This is a target specification intended to support exploration and discussion of critical needs for a proposed or target device. Spec limits including typical, minimum, and maximum values are desired, or target, limits. GLF reserves the right to change limits at any time without warning or notification. A target specification in no way guarantees future production of the device in question.	Design / Development
Preliminary Specification	This is a draft version of a product specification. The specification is still under internal review and subject to change. GLF reserves the right to change the specification at any time without warning or notification. A preliminary specification in no way guarantees future production of the device in question.	Qualification
Product Specification	This document represents the anticipated production performance characteristics of the device.	Production

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