



GLF71301T

Nano-Current Consumed, I_QSmart™ LoadSwitch with Slew Rate Control

Product Specification

DESCRIPTION

The GLF71301T is an ultra-efficiency, 1.5 A rated, Load Switch with integrated slew rate control. The best in class efficiency makes it an ideal choice for use in IoT, mobile, and wearable electronics.

The GLF71301T features an ultra-efficient I_QSmart™ technology that supports the lowest quiescent current (I_Q) and shutdown current (I_{SD}) in the industry. Low I_Q and I_{SD} solutions help designers to reduce parasitic leakage current, improve system efficiency, and increase battery lifetime.

The GLF71301T integrated slew rate control can also enhance system reliability by mitigating bus voltage swings during switching events. Where uncontrolled switches can generate high inrush currents that result in voltage droop and/or bus reset events, the GLF slew rate control specifically limits inrush current during turn-on to minimize voltage droop.

GLF71301T Load Switch device supports an industry leading wide input voltage range and helps to improve operating life and system robustness. Furthermore, one device can be used in multiple voltage rail applications which helps to simplify inventory management and reduces operating cost.

GLF71301T Load Switch device is small utilizing a wafer level chip scale package with 4 bumps in a 0.77 mm x 0.77 mm x 0.35 mm die size and a 0.4 mm bump pitch.

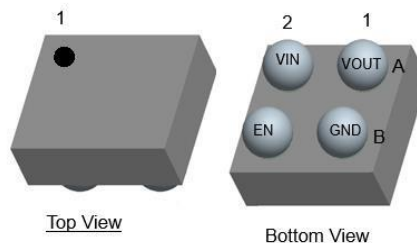
FEATURES

- Ultra-Low I_Q: 1 nA Typ @ 5.5 V_{IN}
- Ultra-Low I_{SD}: 19 nA Typ @ 5.5 V_{IN}
- Low R_{ON} = 34 mΩ Typ. @ 5.5 V_{IN}
- I_{OUT} Max = 1.5 A
- Supply Voltage Range: 1.1 V to 5.5 V
6 V abs max
- Controlled Rise Time: 430 us at 3.3 V_{IN}
- Internal EN Pull-Down Resistor
- Integrated Output Discharge Switch
- Ultra-Small: 0.77 mm x 0.77 mm

APPLICATIONS

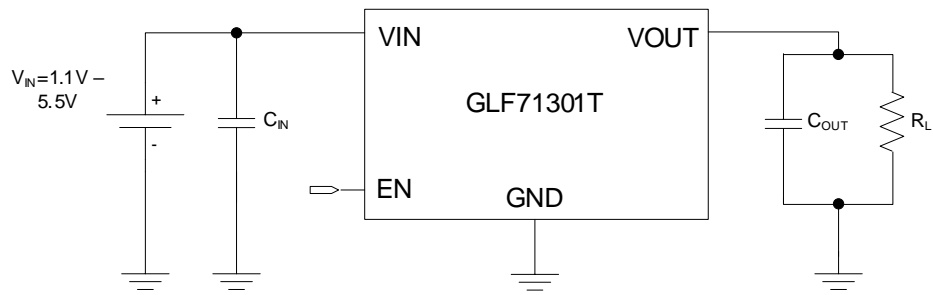
- Wearables
- Data Storage, SSD
- Mobile Devices
- Low Power Subsystems

PACKAGE



0.77 mm x 0.77 mm x 0.35 mm
0.4 mm pitch WLCSP

APPLICATION DIAGRAM



FUNCTIONAL BLOCK DIAGRAM

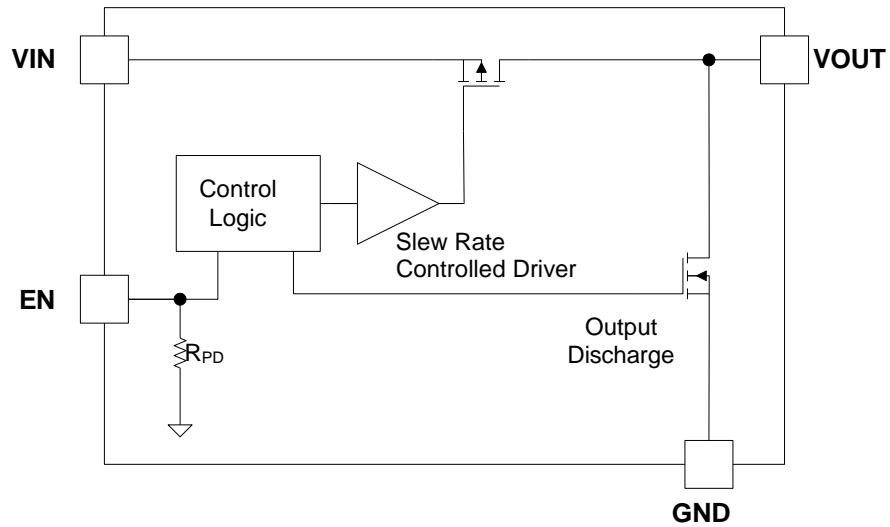
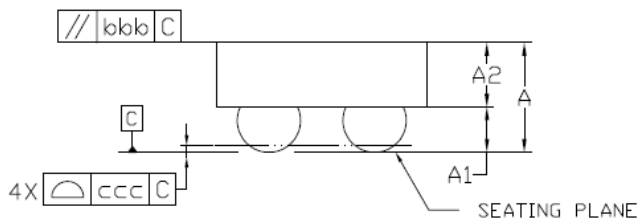
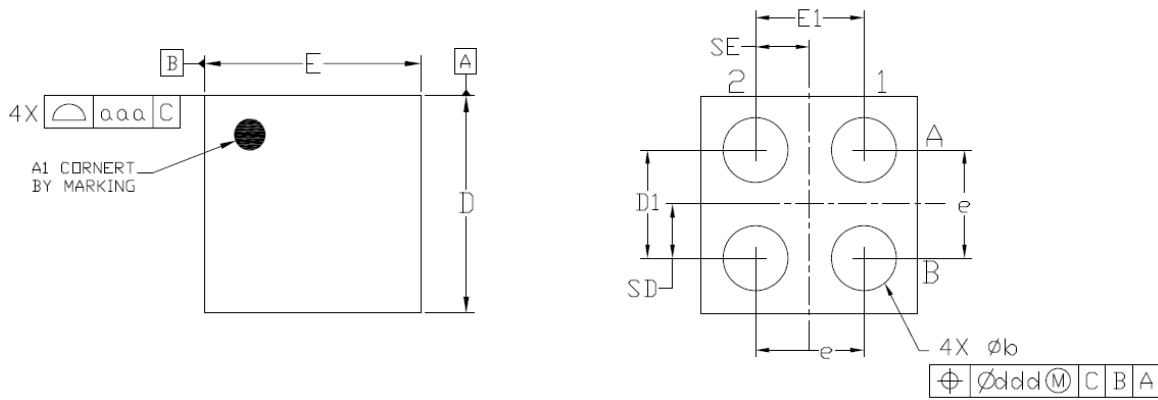


Figure 1. Functional Block Diagram

ULTRA-THIN PACKAGE OUTLINE



Dimensional Ref.			
REF.	Min.	Nom.	Max.
A	0.300	0.350	0.400
A1	0.075	0.100	0.125
A2	0.225	0.250	0.275
D	0.755	0.770	0.785
E	0.755	0.770	0.785
D1	0.350	0.400	0.450
E1	0.350	0.400	0.450
b	0.145	0.180	0.215
e	0.400 BSC		
SD	0.200 BSC		
SE	0.200 BSC		
Tol. of Form&Position			
aaa	0.10		
bbb	0.10		
ccc	0.05		
ddd	0.05		

Notes

1. ALL DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
2. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M-1994.